

QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>09853234</u>	Prepared by _____	Tracking Number <u>0600, 8/10/06</u>	
Examiner-GAU <u>0985 -226</u>	Date _____	Week Date <u>9/6/06</u>	
<u>Courtney</u>	No. of queries _____		

JACKET			
a. Serial No.	f. Foreign Priority	k. Print Claim(s)	<u>p</u> PTO-1449
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	n. PTO-270/328	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

SPECIFICATION	MESSAGE
a. Page Missing	<u>PTO 1449: please initial/line through</u>
b. Text Continuity	<u>Citations dated 10-3-02. Copy provided for</u>
c. Holes through Data	<u>reference.</u>
d. Other Missing Text	
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g. Brief Description	
h. Sequence Listing	
i. Appendix	
j. Amendments	
k. Other	
CLAIMS	
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Form PTO-1449 (modified)

Atty. Docket No.
2000.080000/TT4828Serial No.
09/853,234

List of Patents and Publications for Applicant's

Applicant
Strongin et al.

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Filing Date:
May 11, 2001Group:
2185U.S. Patent Documents
*See Page 1*Foreign Patent Documents
*See Page 1*Other Art
See Page 1

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date of App.
	A1						
	A2						
	A3						
	A4						
	A5						

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Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Country	Class	Sub Class	Translation Yes/No
	B1						
	B2						
	B3						
	B4						
	B5						

Other Art (Including Author, Title, Date Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
	C1	Intel, "Low Pin Count (LPC) Interface Specification Revision 1.0," pp. 1-31 (09/29/97)
	C2	Standard Microsystems Corporation, "100 Pin Enhanced Super I/O for LPC Bus with SMBus Controller for Commercial Application," Part No. LPC47B37x, pp. 1-254 (06/17/99)
	C3	FIPS Pub 140-1 Federal Information Processing Standards Publication, "Security Requirements for Cryptographic Modules" (01/11/1994)
	C4	Intel, "Communication and Networking Riser Specification," Revision 1.0 (02/07/2000)
	C5	"Handbook of Applied Cryptography" CRC Press 1997 pp. 154 - 157, 160 - 161, 191 - 198, 203 - 212

EXAMINER:

DATE CONSIDERED:

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